



Attorney Docket No.: 042390.P7794

GAV

2815

Patent

#5/Election  
6/8/01  
JS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application of:

Murthy, A. et al.

Serial No.: 09/475,452

Filed: December 30, 1999

For: A NOVEL MOS TRANSISTOR  
STRUCTURE AND METHOD OF  
FABRICATION

Examiner: Lee, E.

Group Unit: 2815

TECHNOLGY C

JUN 15 2001

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(37 c.f.r. § 1.8(a))

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231 on Teresa Edwards

June 8, 2001  
Date

RESPONSE TO ELECTION REQUIREMENT

Sir:

In response to the Election Requirement mailed June 1, 2001, applicant(s) hereby elects to prosecute Group I claims, claims 1-15 are drawn to a semiconductor device. As such, applicant(s) elects to withdraw Group II claims, claims 16-18 drawn to method of making a semiconductor device.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Date: 6/8/01

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